

## Product Summary

The SN095 is a fully integrated PLL with charge pump, phase frequency detector, VCO and RF Divider. It is very suitable for applications that require extremely low power. Also integrated in the design is a 4<sup>th</sup> order Delta Sigma Modulator (MASH 1111). It is designed on a 0.18 CMOS process but can be easily ported to other geometries.

## Performance Summary

	Min	Typical	Max	NOTES
Reference Frequency (MHz)	7	10	30	
Programmable RF Divider settings	50		150	
Output Frequency Range(MHz)	880		1100	
Phase Noise @100kHz offset (dBc/Hz)		TBU		
Supply Voltage	1.4		1.8	
Current Consumption		600uA		Excludes Delta Sigma Modulator
Operating Temperature Range(C)	-40	25	85	

### Deliverables

- GDSII Layout
- Footprint (LEF)
- Spice Netlist/ Schematics
- Detailed Technical Data