

Product Summary

The SN097 is a fully integrated PLL with charge pump, phase frequency detector, VCO and RF Divider. It is very suitable for applications that require low output phase noise. An external Delta Sigma Modulator can be used to provide dither control. It is designed on a 0.18 CMOS process but can be easily ported to other geometries.

Performance Summary

	Min	Typical	Max	NOTES
Reference Frequency (MHz)	1		10	
Programmable RF Divider settings	50		150	
Output Frequency Range(MHz)	410		450	
Phase Noise @100kHz offset (dBc/Hz)		TBU		
Supply Voltage	1.4		1.8	
Current Consumption(uA)		500		
Operating Temperature Range(C)	-40	25	85	

Deliverables

- GDSII Layout
- Footprint (LEF)
- Spice Netlist/ Schematics
- Detailed Technical Data