

Product Summary

The SN125 is a programmable 7 bit divider that has extremely low current consumption. Designed for operation upto 700MHz; designed for 0.18 CMOS process and can easily be ported to other processes

Performance Summary

	Min	Typ	Max	Notes
Frequency of Operation (MHz)	10		700	
Supply Voltage (V)		1.8		
Current consumption		<100uA		@700MHz
Input voltage range	1.3		1.8	

Deliverables

- GDSII Layout
- Footprint (LEF)
- Spice Netlist/ Schematics
- Detailed Technical Data